

1746
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar

Title: METHOD OF CLEANING SEMICONDUCTOR SURFACES

Docket No.: 1303.112US1

Filed: October 8, 2003

Examiner: Unknown



Serial No.: 10/681481

Due Date: N/A

Group Art Unit: 1746

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

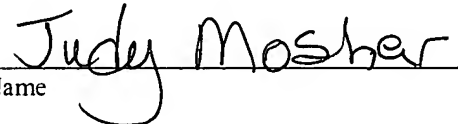
☒ An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copy of 1 cited document.

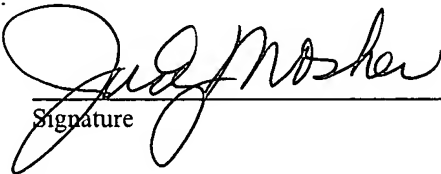
If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: 
Atty: David C. Peterson
Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this ____ day of March, 2004.


Name


Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 10/681481



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|-------------|---|-----------------|-------------|
| Applicant: | Paul A. Farrar | Examiner: | Unknown |
| Serial No.: | 10/681481 | Group Art Unit: | 1746 |
| Filed: | October 08, 2003 | Docket: | 1303.112US1 |
| Title: | METHOD OF CLEANING SEMICONDUCTOR SURFACES | | |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/681481

Filing Date: October 08, 2003

Title: METHOD OF CLEANING SEMICONDUCTOR SURFACES

Page 2

Dkt: 1303.112US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

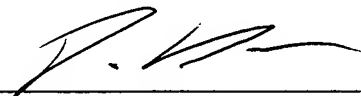
Respectfully submitted,

PAUL A. FARRAR

By his Representatives,

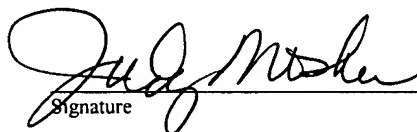
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6944

Date 3-19-04

By 
David C. Peterson
Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this ____ day of March, 2004.

Judy Mosher
Name


Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number 10/681481

Filing Date October 8, 2003

First Named Inventor Farrar, Paul

Group Art Unit 1746

Examiner Name Unknown

Sheet 1 of 1

Attorney Docket No: 1303.112US1

US PATENT DOCUMENTS

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
|--------------------|---------------------|------------------|---|-------|----------|----------------------------|
| | US-4,817,652 | 04/04/1989 | Liu, Benjamin Y. | 134 | 102.1 | 03/26/1987 |
| | US-4,944,837 | 07/31/1990 | Nishikawa, Masaru , et al. | 216 | 41 | 02/28/1989 |
| | US-4,962,776 | 10/16/1990 | Liu, Benjamin Y., et al. | 134 | 11 | 05/03/1988 |
| | US-5,185,296 | 02/09/1993 | Morita, Kiyoyuki , et al. | 438 | 694 | 04/24/1991 |
| | US-5,304,515 | 04/19/1994 | Morita, Kiyoyuki , et al. | 437 | 231 | 08/07/1992 |
| | US-5,900,354 | 05/04/1999 | Batchelder, John S. | 430 | 395 | 07/03/1997 |
| | US-6,071,439 | 06/06/2000 | Bawa, Rajan S., et al. | 264 | 1.1 | 09/23/1997 |
| | US-6,149,828 | 11/21/2000 | Vaartstra, Brian A. | 216 | 57 | 05/05/1997 |
| | US-6,242,165 | 06/05/2001 | Vaartstra, Brian A. | 430 | 329 | 08/28/1998 |
| | US-6,306,754 | 10/23/2001 | Agarwal, Vishnu K. | 438 | 619 | 06/29/1999 |
| | US-6,666,986 | 12/23/2003 | Vaartstra, Brian A. | 252 | 79.1 | 07/14/2000 |

FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T ² |
|--------------------|---------------------|------------------|---|-------|----------|----------------|
|--------------------|---------------------|------------------|---|-------|----------|----------------|

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|--------------------|----------------------|---|----------------|
| | | WOLF, "SILICON PROCESSING FOR THE VLSI ERA", <u>Process Integration</u> , Vol. I, (1989), 169-171 | |

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached